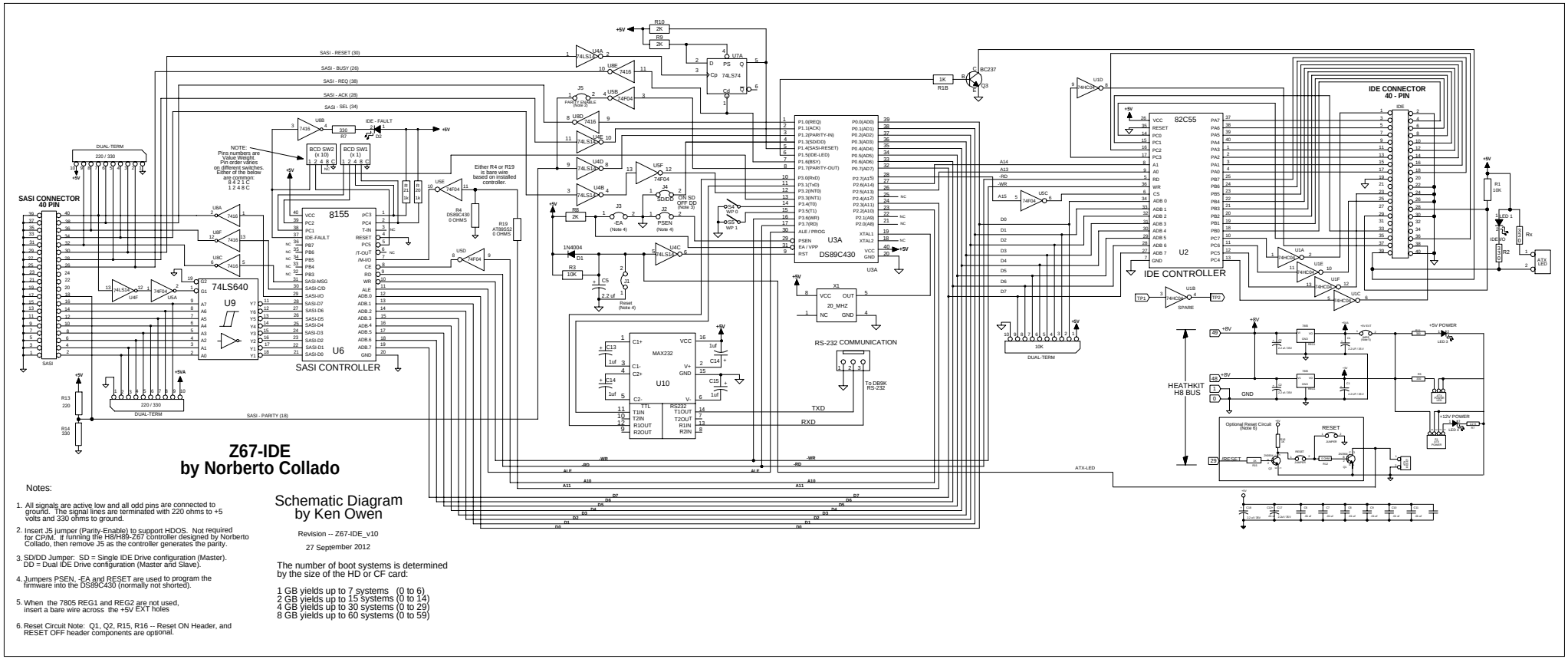

Notes:

1. All signals are active low and all odd pins are connected to ground. The signal lines are terminated with 220 ohms to +5 volts and 330 ohms to ground.
2. Insert J5 jumper (Parity-Enable) to support HDOS. Not required for CP/M, if running the H8/H89-Z67 controller designed by Norberto Collado, then remove J5 as the controller generates the parity.
3. SD/DD Jumper: SD = Single IDE Drive configuration (Master). DD = Dual IDE Drive configuration (Master and Slave).
4. Jumpers PSEN, -EA and RESET are used to program the firmware into the DS89C430 (normally not shorted).
5. When the 7805 REG1 and REG2 are not used, insert a bare wire across the +5V EXT holes
6. Reset Circuit Note: Q1, Q2, R15, R16 -- Reset ON Header, and RESET OFF header components are optional.

The number of boot systems is determined by the size of the HD or CF card:

1 GB yields up to 7 systems (0 to 6)
2 GB yields up to 15 systems (0 to 14)
4 GB yields up to 30 systems (0 to 29)
8 GB yields up to 60 systems (0 to 59)