

System Support I Board - Jumper Settings

v1.0 8/9/2021

Do not install header or jumper here

Default jumper setting

JP1	RD_L	1-2 (RIGHT)	Use delayed READ	2-3 (LEFT)	Use READ from bus	
JP2	WR_L	1-2 (RIGHT)	Use delayed WRITE	2-3 (LEFT)	Use WRITE from bus	
JP3	H17_BANK	1-2 (RIGHT)	FP LED shows H17 side select	2-3 (LEFT)	FP LED shows Bank Select bit	
JP4	IO_WAIT	ON	Use I/O Wait States	OFF	No I/O Wait States	
JP5	MEM_WAIT	ON	Use Memory Wait States	OFF	No Memory Wait States	
JP6	APU_WAIT	ON	Use APU (AM9511) Waits	OFF	No APU Wait States	
JP7	END_SVREQ	1-2 (LEFT)	Generate interrupt on APU END	2-3 (RIGHT)	Generate interrupt on APU SVREQ	
JP8	RDYIN*	ON	Pullup RDYIN* to +5v	OFF	CPU board does pullup	
JP9	+18V/+12V	ON	Use +12v from bus	OFF	Use onboard +12v regulator	
JP10	-18V/-12V	ON	Use -12v from bus	OFF	Use onboard -12v regulator	
JP11	LINE_FEED	ON	PC2 can be used as LINEFEED	OFF	PC2 is not used	
JP12	RESET	ON	PC1 can be used as RESET	OFF	PC1 is not used	
JP13	AGRD_GRD	1-2 (LEFT)	P10 referenced to AGRD	2-3 (RIGHT)	P10 referenced to GND	
JP14	MEM_MAX	ON	Use Mem Waits at Max Speed	OFF	Use Mem Waits at all CPU speeds	
JP15	BUSY_SEL	1-2 (LEFT)	Busy on PC7 (Mode 0)	2-3 (RIGHT)	Busy on PC4 (Mode 1)	
JP16	IORQ	1-2 (BOTTOM)	Use Z80 IORQ on bus pin 24	2-3 (TOP)	Fake IORQ from IORD + IOWR	
JP19	8V/5V	ON	Use +5v from bus	OFF	Use onboard +5v regulator	
JP20	8V/5V	ON	Use +5v from bus	OFF	Use onboard +5v regulator	
JP23	BUFFER-ON/OFF	1-2 (LEFT)	Use bus buffers	2-3 (RIGHT)	Do not use bus buffers (test mode)	
P11	I/O WAIT	1-2 (LEFT)	Generate IOW wait states	3-4 (CENTER)	Generate IOR wait states	5-6 (RIGHT) Generate IORQ wait states
P14	MEM WAIT	1-2 (LEFT)	Generate M1 wait states	3-4 (CENTER)	Generate MEMR wait states	5-6 (RIGHT) Generate MEMW wait states

Three Interrupt Jumpers		1-2 (TOP)	Use INT6
P9	ADC-INT	3-4	Use INT5
P15	APU-INT	5-6	Use INT4
P16	PARALLEL-INT	7-8 (BOTTOM)	Use INT3

R49-R53	222ohm LED resistors	Install wire jumper at R49-R53 if using Front Panel 2.0 LEDs
---------	----------------------	--