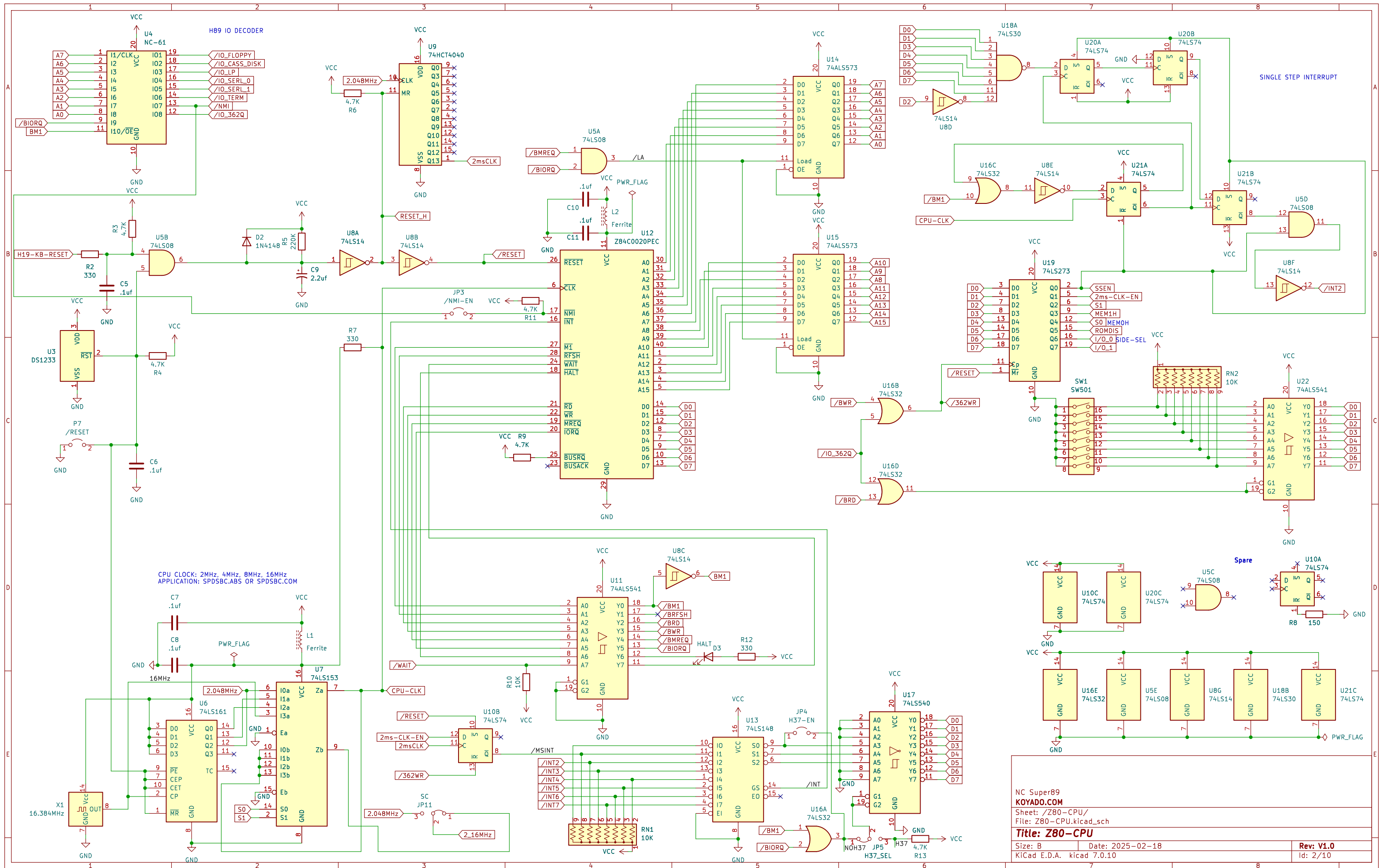




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Sheet: /Z80-CPU/

File: Z80-CPU.kicad_sch

Title: Z80-CPU

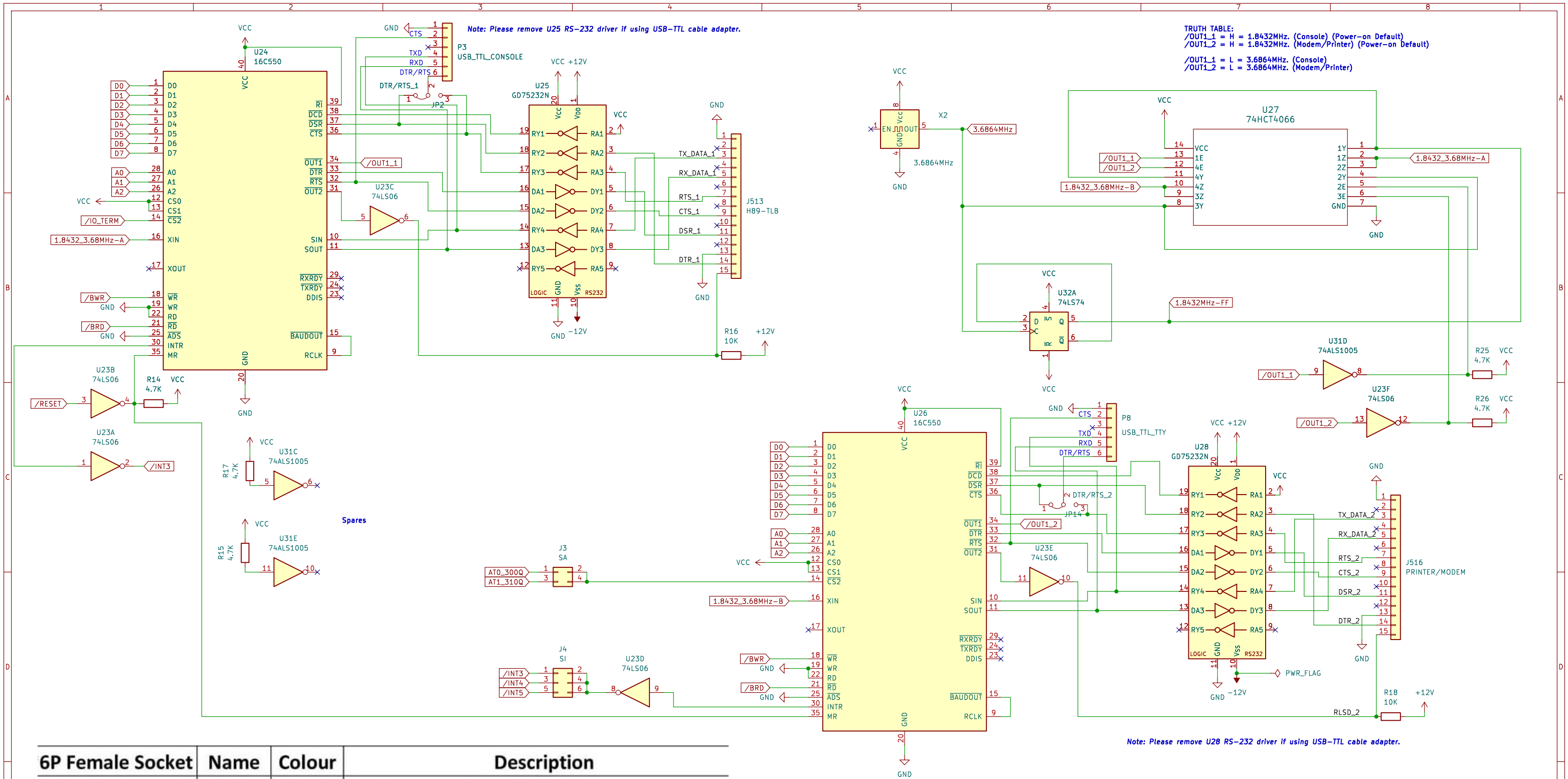
Size: B

Date: 2025-02-18

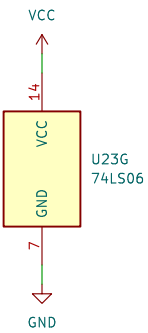
Rev: V1.0

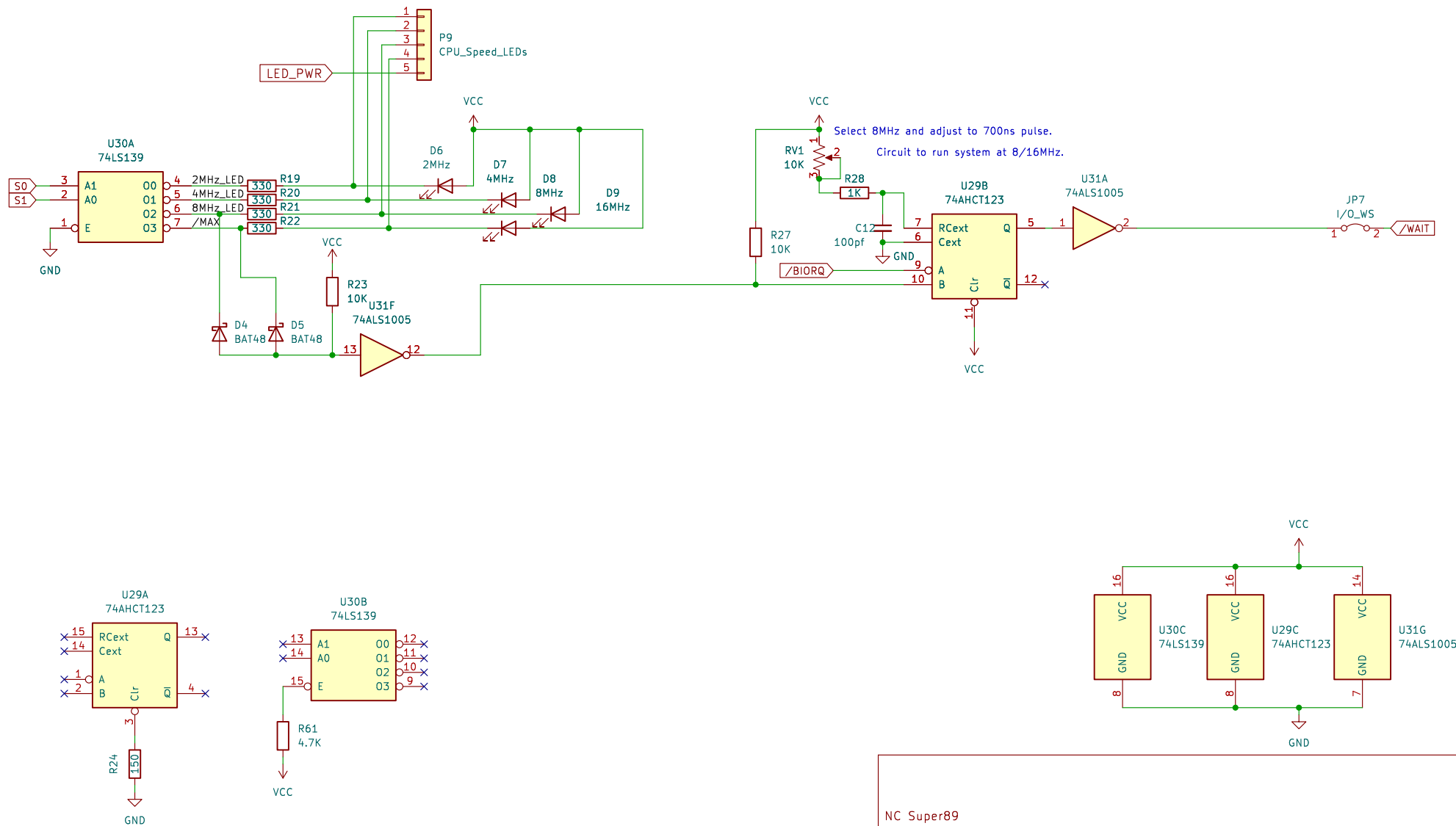
KiCad E.D.A. kicad 7.0.10

Id: 2/10



6P Female Socket	Name	Colour	Description
Pin 1	GND	Black	Device ground supply
Pin 2	CTS	Brown	Clear to Send Control/Handshake signal
Pin 3	VCC	Red	+5V
Pin 4	TXD	Orange	Transmit Asynchronous Data
Pin 5	RXD	Yellow	Receive Asynchronous Data
Pin 6	RTS	Green	Request To Send Control/Handshake signal





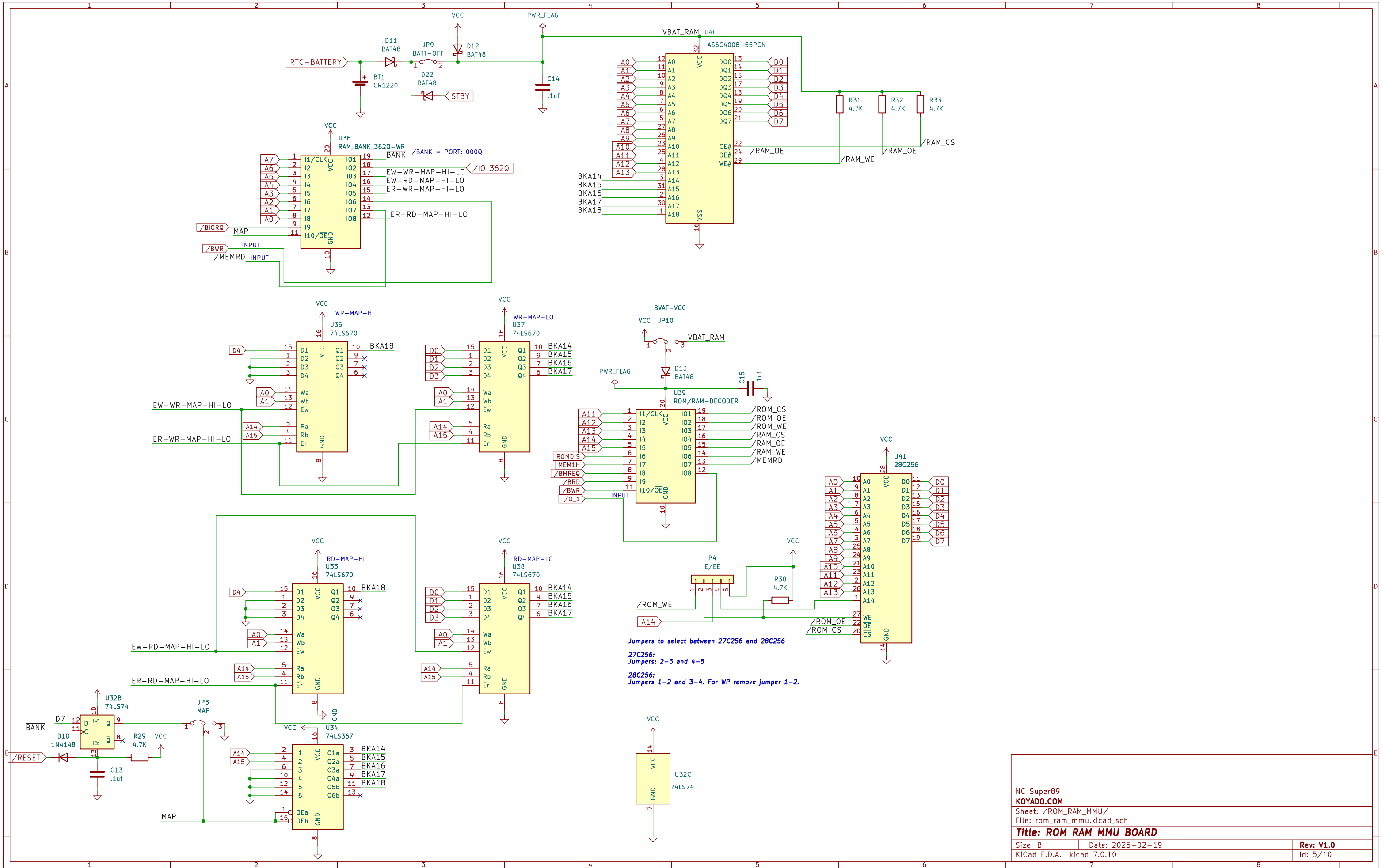
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Sheet: /WAIT_STATE_GENERATOR/
File: wait_state_generator.kicad_sch

Title: WAIT STATE GENERATOR TO RUN AT 8/16MHz

Size: A4 Date: 2025-02-19
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Rev: v1.0
Id: 4/10



The Wiznet and SD I/O board will be an standalone board.
Check with Norberto for availability.

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Sheet: /WIZNET_SD_CARDS/
File: wiznet_sd_cards.kicad_sch

Title:

Size: A4
KiCad E.D.A. kicad 7.0.10

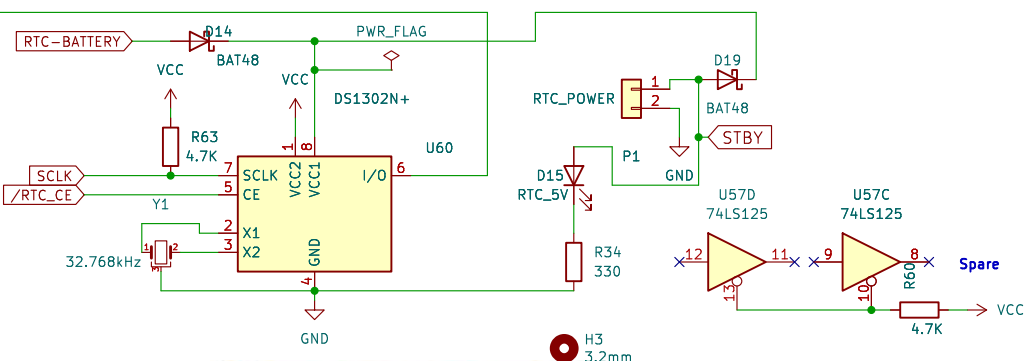
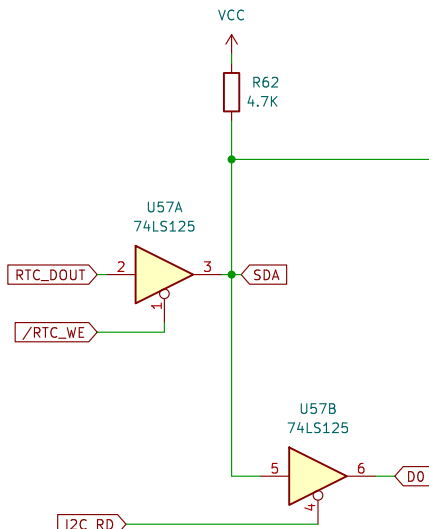
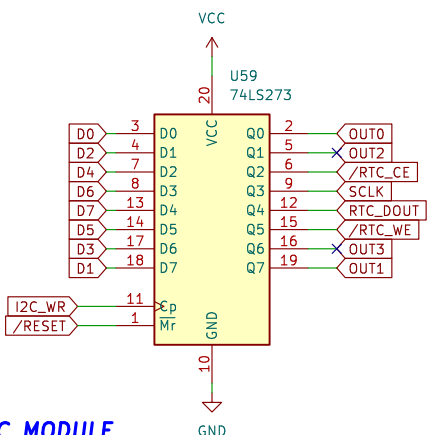
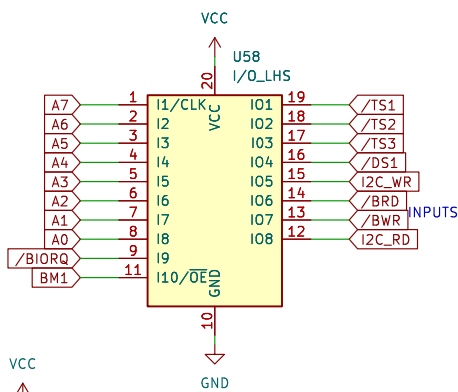
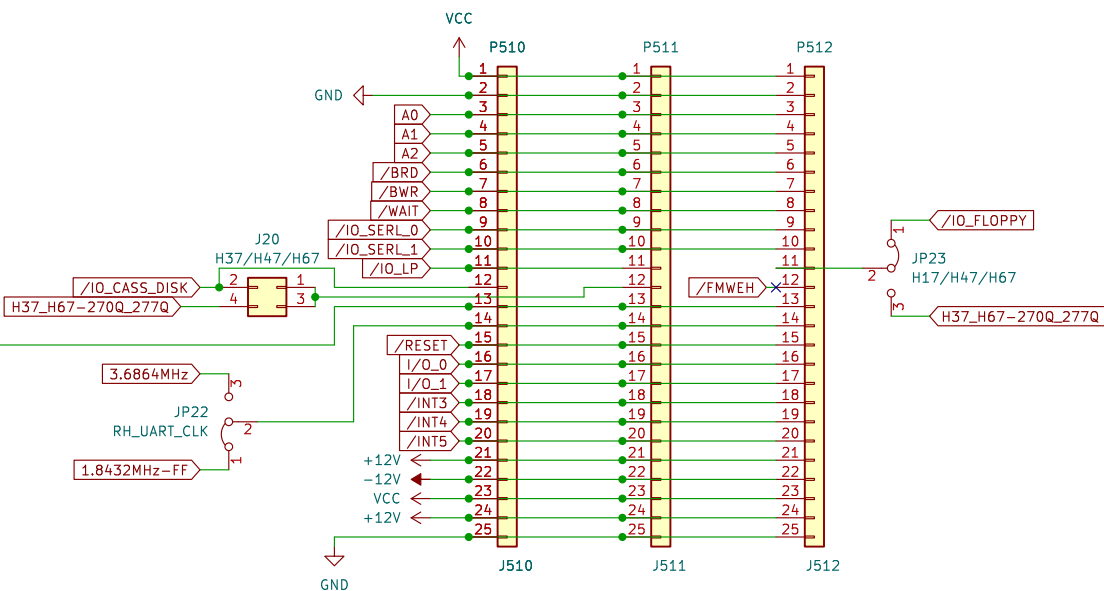
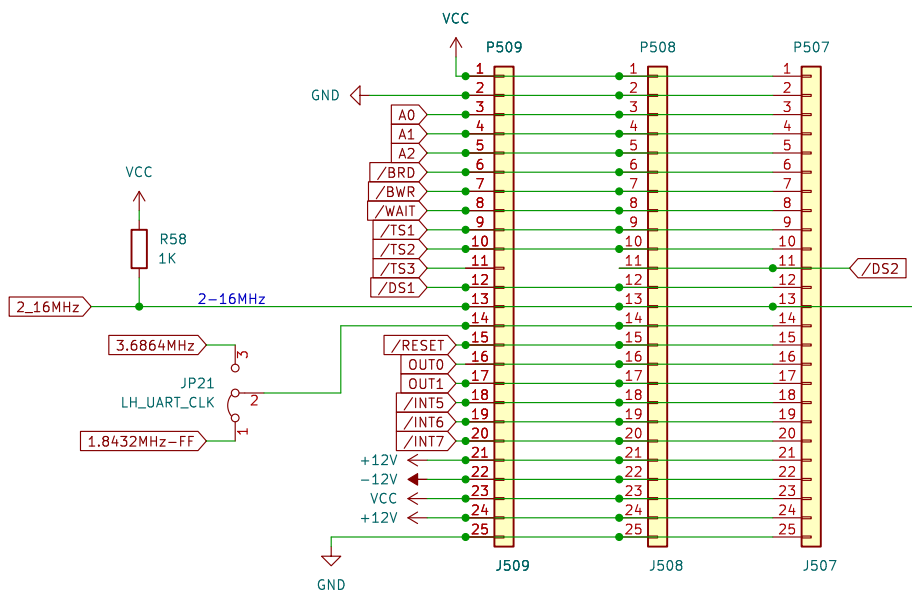
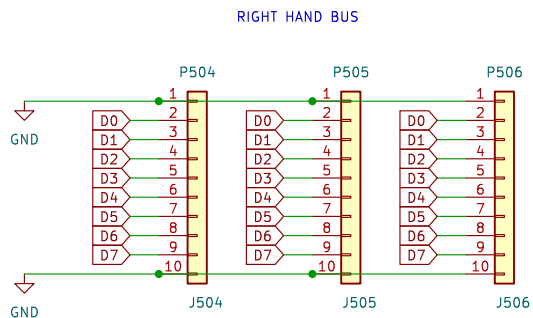
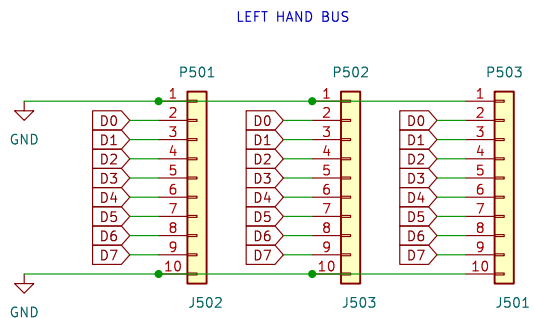
Date:

Rev:

Id: 6/10

LEFT-HAND BUS DEVICE SELECT PORTS:

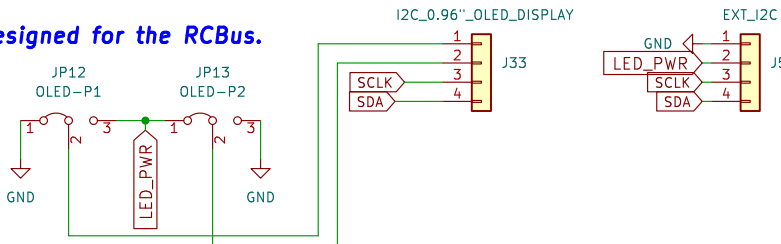
RTC-I2C: 200Q (80H)
TS1: 210Q->217Q (88H->8FH)
TS2: 220Q->227Q (90H->97H)
TS3: 230Q->237Q (98H->9FH)
DS1: 240Q->247Q (A0H->A7H)
DS2: 250Q->257Q (A8H->AFH)

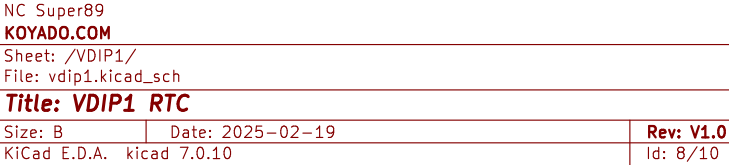


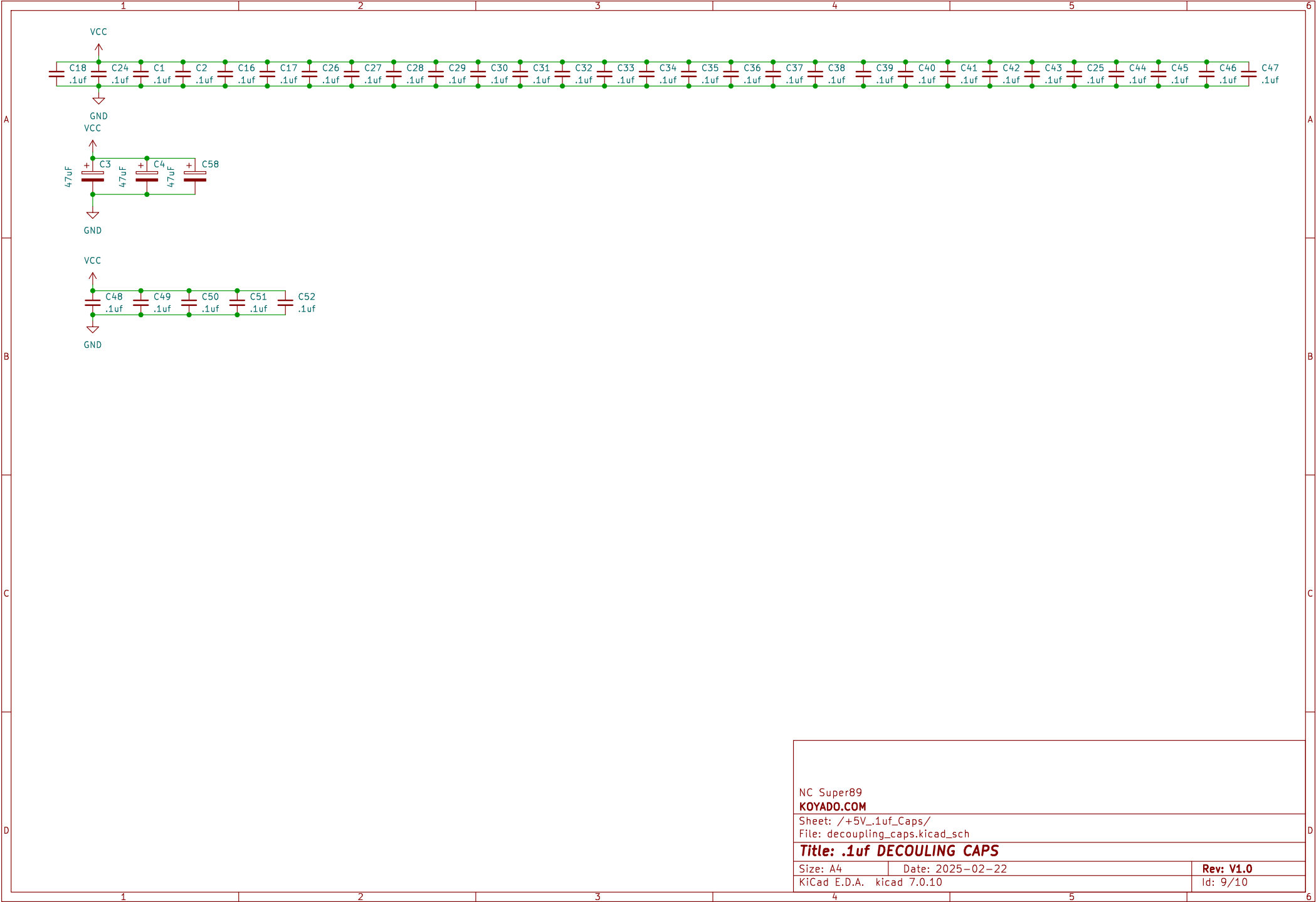
SC727 - RCBUS RTC MODULE

SC727 is a DS1302 based real time clock designed for the RCBus.

PORT:
RTC-I2C: 200Q (80H)







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Sheet: /+5V_.1uF_Caps/
File: decoupling_caps.kicad_sch

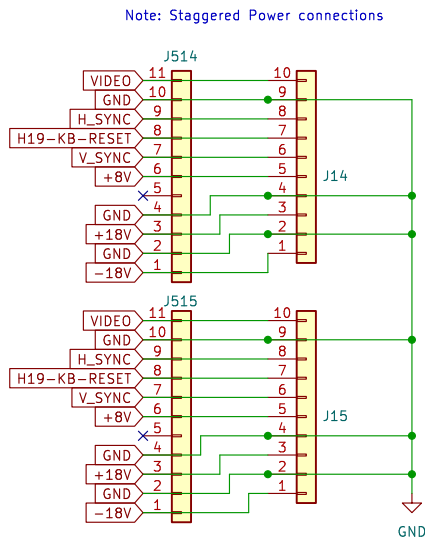
Title: .1uF DECOULING CAPS

Size: A4 Date: 2025-02-22

KiCad E.D.A. kicad 7.0.10

Rev: V1.0

Id: 9/10



Rev: 1.0
Id: 10/10