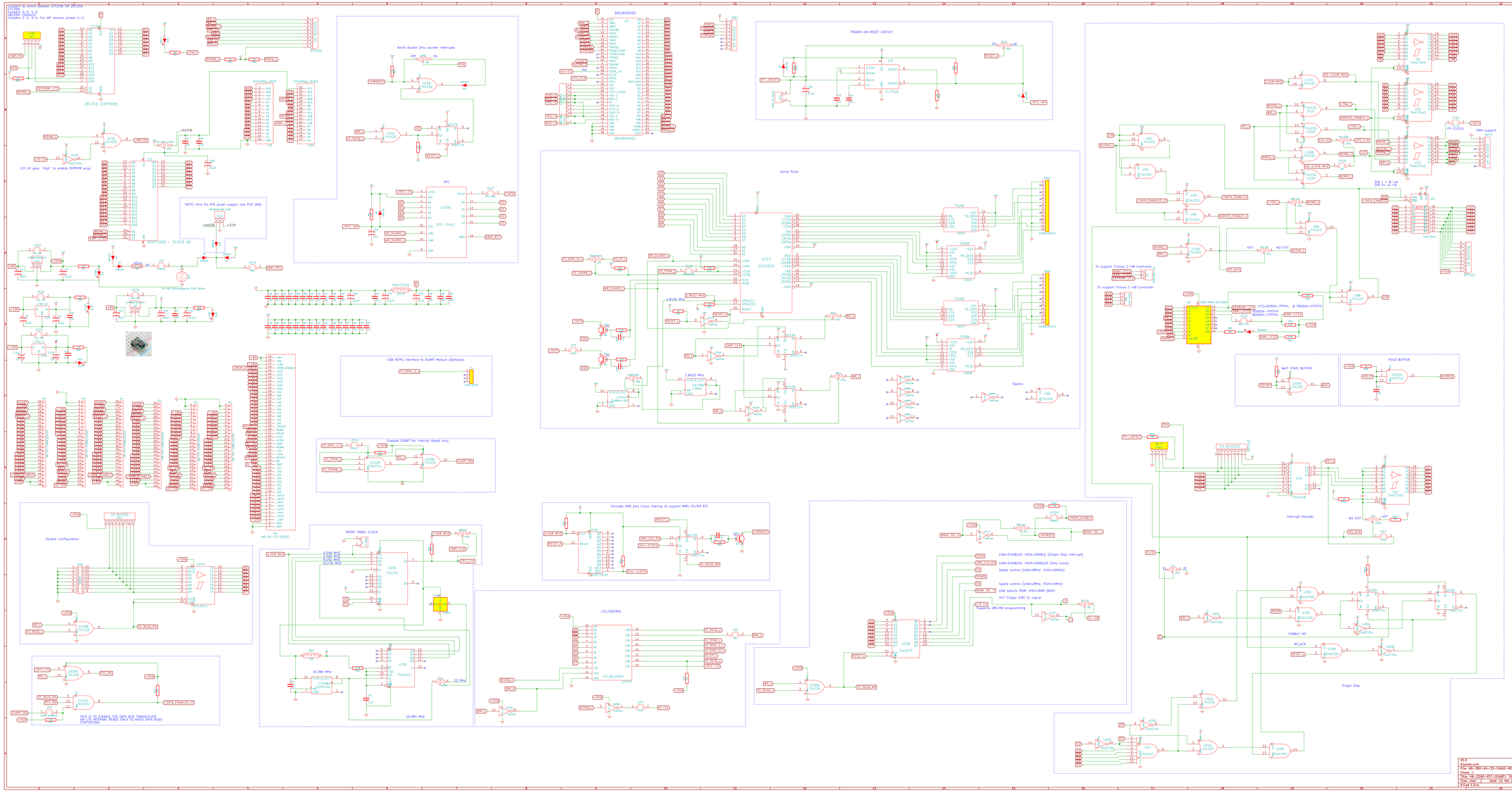
Jumpers to select between 27C256 OR 28C256
27C256:
Jumpers 1-2, 3-4, 5-6
28C256:
Jumpers 1-2, 3-4, for WP remove jumper 5-6
28C256 (EEPROM)
I/O IH goes 'High' to enable EEPROM prog.
Avoid double 2ms counter interrupts
RTC
NOTE: Only for ATX power supply. Use P10 (KB).
X-RAM-CARD - 512KB X8
Ni-MH Rechargeable 3.6V 80ma
USB VDIP1 Interface to UART Module (Optional)
Enables UART for Internal Reads only.
H8-P1-P2-BUSS
System Configuration
FRONT PANEL CLOCK
16.384 MHz
12 MHz
THIS IS TO DISABLE THE DATA BUS TRANSCEIVER
WHEN EXTERNAL READS ONLY TO AVOID DATA BUSS
CONTENTION
Emulate H89 2ms Clock Interrup to support MMS CP/M3 RTC
I/O_CONTROL
I/O_DECODER
POWER-ON RESET CIRCUIT
Serial Ports
Spares
LOW=DISABLED, HIGH=ENABLE (Single Step Interrupt)
LOW=DISABLED, HIGH=ENABLED (2ms clock)
Speed control (LOW=8MHz, HIGH=16MHz)
Speed control (LOW=2MHz, HIGH=4MHz)
LOW selects ROM, HIGH=RAM (64K)
H17 Floppy SIDE 1 signal
supports 28C256 programming
To support Trionyx C-HB Controller
To support Trionyx C-HB Controller
DMA support
DIP L = B-x4
DIP H= A-x8
/CS=0000H-7FFFH, & F8000H-FFFFFH
8000H-7FFFH
WAIT STATE BUFFER
HOLD BUFFER
Interrupt Decoder
NO H37
ENABLE INT
INT_ACK
Single Step
V5.0
Kopako.com
File: H8-280-64-33-SMGE-ROM-RTC.sch
Sheet: /
Title: H8-280-64-33-SMGE-ROM-RTC.sch
Size: User Date: 11 Feb 2023 Rev: 1.0
KiCad E.D.A. Id: 1/1