

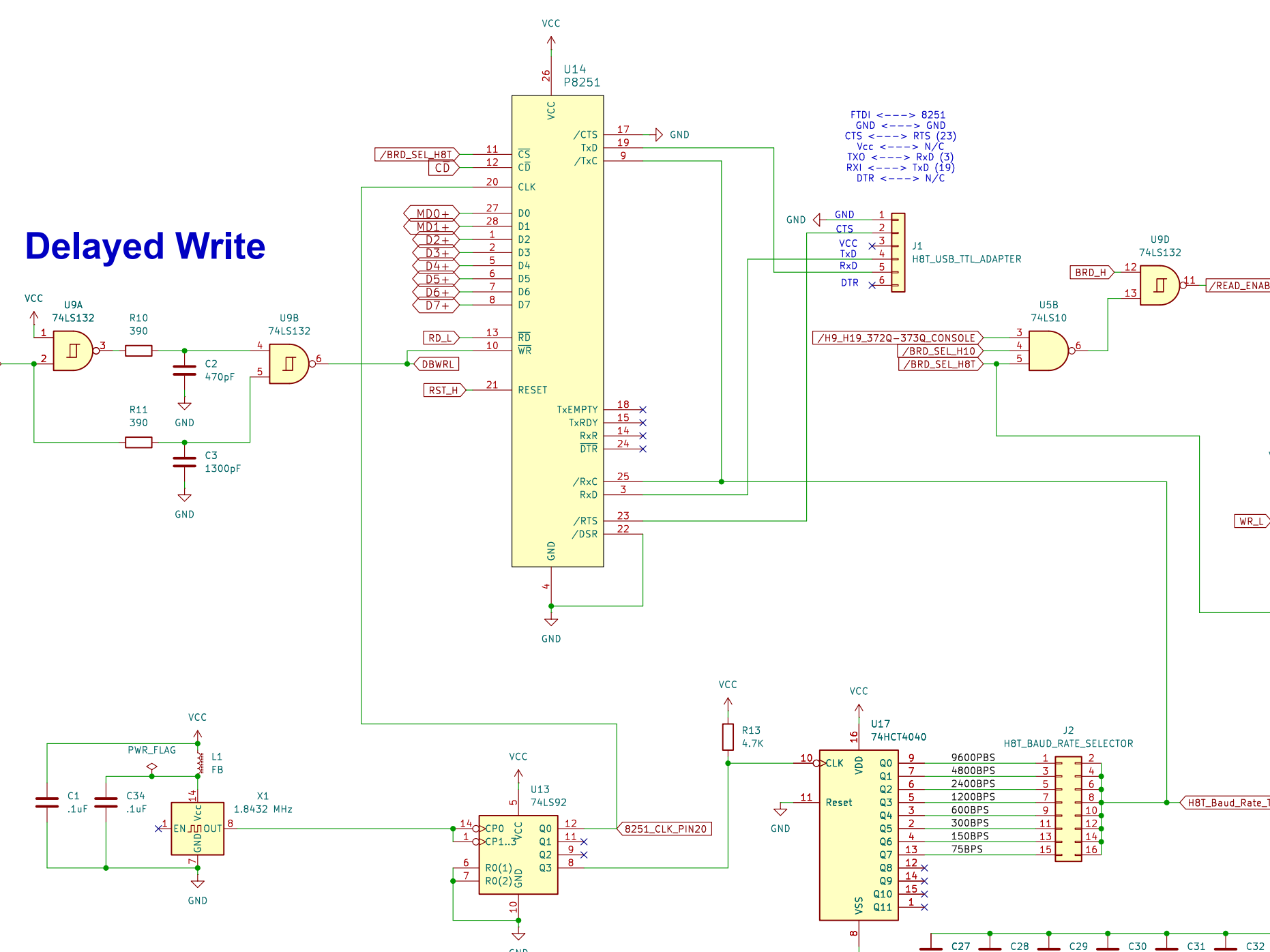
H-8-2 PARALLEL I/O

H8 to H10 INTERFACE TABLE

Refer to Pictorial 3-3 for the following table.
 NOTE: The following table indicates the configuration for channel φ on the H10 Interface.
 Address — 370
 A1-A2 OPEN
 H1-H2 OPEN
 C1-C2 INSTALLED
 E1-E2 INSTALLED
 F, G, and H — OPEN (unless called for in the software instruction).

H8-2 Interface			H10 Interface		
Function	Pin	Wire color	Signal Flow	Pin	Function
Data φ	1	Black	→	11	Data φ
Data 1	2	Brown	→	12	Data 1
Data 2	3	Red	→	13	Data 2
Data 3	4	Orange	→	14	Data 3
Data 4	5	Yellow	→	15	Data 4
Data 5	6	Green	→	16	Data 5
Data 6	7	Blue </td <td>→</td> <td>17</td> <td>Data 6</td>	→	17	Data 6
Data 7	8	Violet	→	18	Data 7
Ground	9	Gray	→	24	Ground
Data Taken	10	White	→	21	Punch Ready
Take Data	11	Black	→	20	Punch Command
Device Control	12	Brown	→	22	N/C
Device Ready	13	Red	→	19	Punch Ready
Device Control	14	Orange	→	23	N/C
Send Data	15	Yellow	→	10	Reader Start
Data Sent	16	Green	→	9	Reader Ready
Ground	17	Blue	→	24	Ground
Data 7	18	Violet	→	8	Data 7
Data 6	19	Gray	→	7	Data 6
Data 5	20	White	→	6	Data 5
Data 4	21	Black	→	5	Data 4
Data 3	22	Brown	→	4	Data 3
Data 2	23	Red	→	3	Data 2
Data 1	24	Orange	→	2	Data 1
Data φ	25	Yellow	→	1	Data φ

H8T-USB-SERIAL-PORT



Delayed Write

H8T P8251 MODE CONTROL AFTER RESET

H9/H19 BAUD RATE

BAUD RATE GENERATOR

H9-H19 CONSOLE TERMINAL

H9 H19 CONSOLE - 372Q & 373Q

