APU-H ARITHMETIC PROCESSOR CARD



The APU-H is a high performance processor card that greatly enhances the computational capability of the H8 system. Built around the AMD9511A arithmetic processor integrated circuit, the APU-H features 16 and 32 bit fixed-point as well as 32 bit floating point arithmetic. The APU-H performs many complex operations such as calculating square roots, evaluating trig, inverse trig and log functions and raising numbers to a power, as well as basic addition, subtraction, multiplication and division. Also included in the capabilities of the APU-H are a fixed-point to floating-point conversion function and a floating-point to fixed-point function.

The APU-H is easy to use and is accessible as an I/O port by the H8. Data transfer can be by programmed I/O or it can be interrupt driven. Contributing to the ease of use and versatility of the APU-H is a software package which replaces the standard Extended BASIC math routines with APU-H routines. Additionally, an interface based on the BASIC USR function allows expansion of the BASIC math functions (e.g., inverse SINE and COSINE). The APU-H user guide also contains a discussion and examples of usage in assembler language.

The APU-H can offer substantial performance gains (2 to 10 times faster) for many programs that are principally computational in nature. Nominal execution times, for example, are 8 us for a 16 bit fixed-point add and 27 us for a 32 bit floating-point add.

The APU-H is completely compatible with the H8 specifications for both physical size and bus (edge) connectors. The APU-H comes completely assembled and tested and is priced at \$389.00.

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