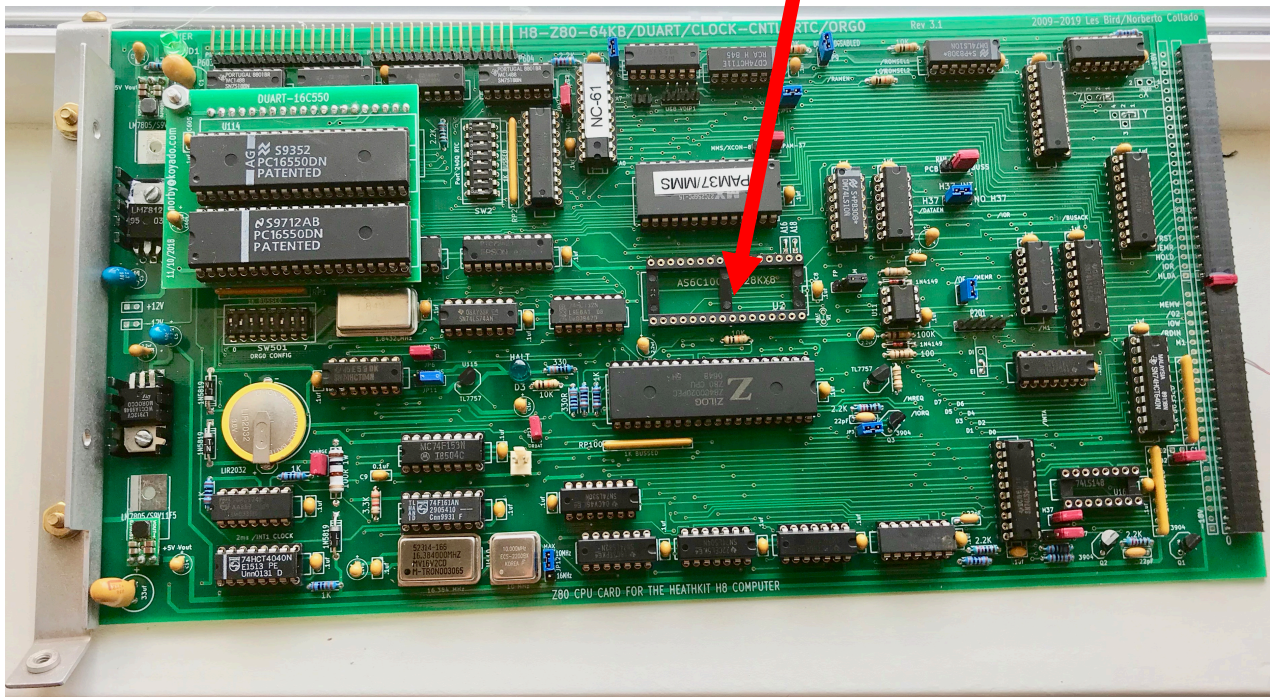
Warning!

When using memory boards on the H8 backplane and if using Les's Z80 board, the On-Board RAM at location U2 (AS6C1008-128Kx8) must be removed. Moving the "RAM CONFIG" jumper to the "BUSS" location, **will not disabled the On-Board RAM (U2)**, causing contention with external memory boards. What the jumper does is that it enables memory reads from the H8 bus. The picture below shows the memory at location U2 removed to be able to use RAM on the H8 bus without any issues.

U2 removed

Z80 V3.1 Board



H8 512KB RAM MMU Jumpers

Jumper	Function
JP1	MAP Jumper Position 1-2 enables full 512KB range (default position) Position 2-3 enables only 64KB of RAM.
JP2	CHARGE Jumper ON when using NiMH batteries OFF when using LIR2032 or CR2032 coin battery (default)
JP3	RBAT Jumper – When inserted provides battery backup to RAM (default configuration). Only remove when replacing the 512KB RAM IC.
JP4	Pulse Detector – Used to eliminate spikes on the I/O ports during power-on. Position 1-2 “PDD-ON”, used when running only at 2MHZ. Position 2-3 “PDD-OFF”, default configuration when running CPU speeds @2-10/16MHz.
JP5	RAM CS# Jumper – When inserted enables the RAM (default position)
JP6	Delayed Write Position 1-2 “ON” (default position) – delayed write cycle. Position 2-3 “OFF”, normal write cycle.
JP7	/DEBUG – Not used (do not install header).
P7	BANK Debug header. No need to install 5 pin header.
W1	VBAT – Test Point to measure RAM battery backup voltage when system is ON or OFF.

H8 512KB RAM MMU DIP SWITCH

PORT	SWITCH SETTINGS
000Q->007Q 00H->07H	SW#1->SW#8 – Set to the “ON” position.